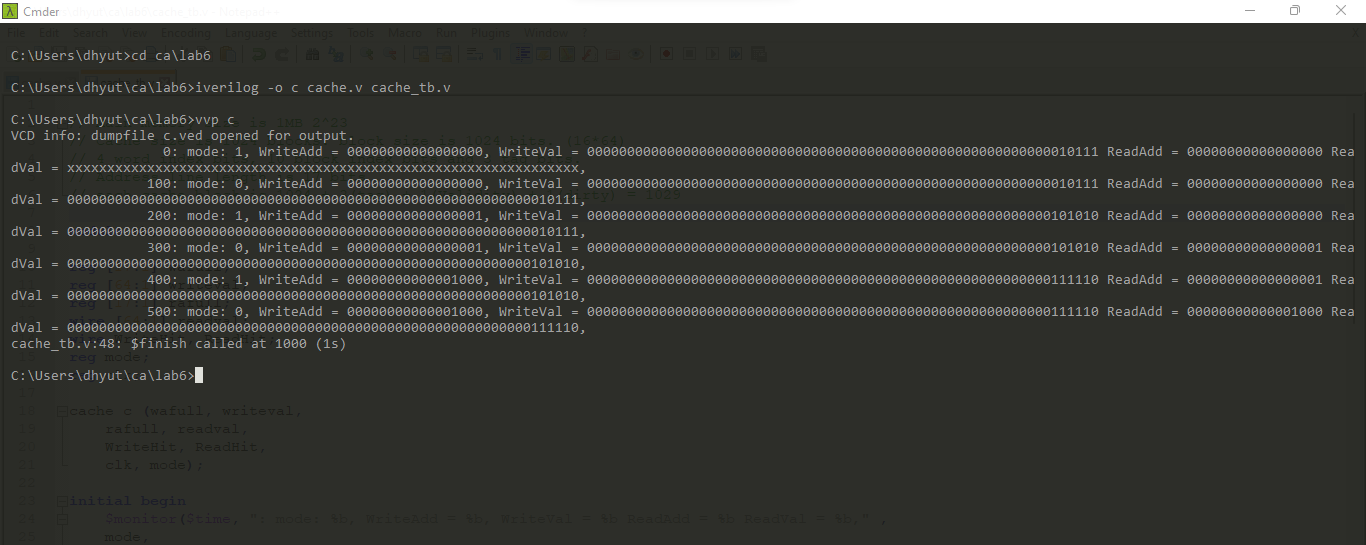
**Computer Architecture Practice - COM307P**

**Lab - 6**

# **Design of Direct Cache Memory**

**Done by : N Sree Dhyuti - CED19I027**

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**THE END**